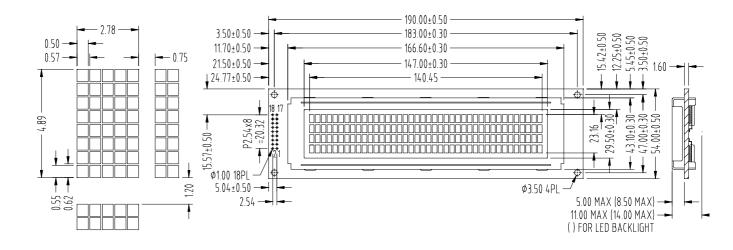
# **PRODUCT SPECIFICATIONS**

- PHYSICAL DATAEXTERNAL DIMENSIONS
- BLOCK DIAGRAM
- ABSOLUTE MAXIMUM RATINGS
- ELECTRICAL CHARACTERISTICS
- OPERATING PRINCIPLES & METHODS
- DISPLAY DATA RAM ADDRESS MAP
- ELECTRO-OPTICAL CHARACTERISTICS
- INTERFACE PIN CONNECTIONS
- CIRCUIT DIAGRAM
- RELIABILITY
- QUALITY GUARANTEE
- INSPECTION CRITERIA
- PRECAUTIONS FOR USING LCD MODULES
- USING LCD MODULES

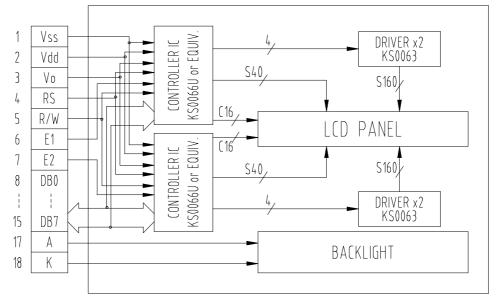
### **PHYSICAL DATA**

Item	Contents	Unit
LCD type	TN / STN / FSTN	
LCD duty	1/16	
LCD bias	1/5	
Viewing direction	6 / 12	o'clock
Module size (W×H×T)	$190 \times 54 \times 11.0$ MAX (14.0 MAX W/LED BACKLIGHT)	mm
Viewing area (W×H)	$147 \times 29.5$	mm
Number of characters (characters×lines)	$40 \times 4$	
Character matrix (W×H)	$5 \times 8$	dots
Character size (W×H)	$2.78 \times 4.89$	mm
Dot size (W×H)	$0.50 \times 0.55$	mm
Dot pitch (W×H)	$0.57 \times 0.62$	mm

# EXTERNAL DIMENSIONS



### **BLOCK DIAGRAM**



Parameter	Symbol	Min	Max	Unit
Supply voltage for logic	VDD	-0.3	7.0	V
Supply voltage for LCD	VDD - VO	-0.3	VDD+0.3	V
Input voltage	VI	-0.3	VDD+0.3	V
Normal operating temperature	TOP	0	50	°C
Normal storage temperature	TST	-10	60	°C
Wide operating / storage temperature (except FSTN)	TOP / TST	-30	80	°C
Wide operating / storage temperature (FSTN)	TOP / TST	-30	70	°C

# **ABSOLUTE MAXIMUM RATINGS** ( $Ta = 25^{\circ}C$ )

# **ELECTRICAL CHARACTERISTICS** (VDD = $+5V\pm10\%$ , VSS = 0V, Ta = $25^{\circ}C$ )

### ♦ DC Characteristics

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Supply voltage for logic	VDD		4.5	5.0	5.5	V
Supply current for logic	IDD			2.51	5	mA
Operating voltage for LCD	VDD - VO	25°C	4.5	4.8	5.1	V
Input voltage ' H ' level	VIH		VDD - 2.2		VDD	V
Input voltage ' L ' level	VIL		0		0.8	V

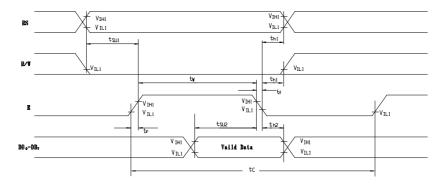
• Backlight operating information (Ta =  $25^{\circ}C$ )

	Sup	ply voltage VF	F (V)	Supp	ly current IF	(mA)
LED Backlight	Min	Тур	Max	Min	Тур	Max
Side lit backlight Y/G (-2)		4.2	4.6		320	540
	EL Enab	ole voltage EO	N (VAC)	EL f	frequency LF	(Hz)
EL Backlight	Min	Тур	Max	Min	Тур	Max
EL (B)		100	150		400	1000

### ♦ AC Characteristics

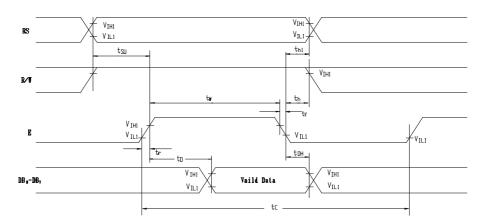
• Write mode

Characteristic	Symbol	Min.	Тур.	Max.	Unit	Test pin
E cycle time	t <sub>C</sub>	500			ns	Е
E rise time	t <sub>r</sub>			25	ns	Е
E fall time	t <sub>f</sub>			25	ns	Е
E pulse width (High, Low)	t <sub>w</sub>	220			ns	Е
R/W and RS set-up time	t <sub>SU1</sub>	40			ns	R/W, RS
R/W and RS hold time	t <sub>h1</sub>	10			ns	R/W, RS
Data set-up time	t <sub>SU2</sub>	60			ns	$DB_0 \sim DB_7$
Data hold time	t <sub>h2</sub>	10			ns	$DB_0 \sim DB_7$



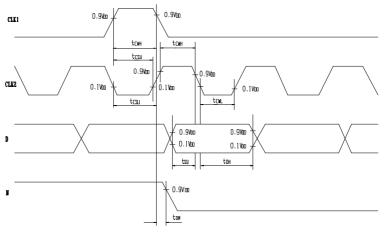
#### • Read mode

Characteristic	Symbol	Min.	Тур.	Max.	Unit	Test pin
E cycle time	t <sub>C</sub>	500			ns	Е
E rise time	t <sub>r</sub>			25	ns	Е
E fall time	t <sub>f</sub>			25	ns	Е
E pulse width	t <sub>w</sub>	220			ns	Е
R/W and RS set-up time	t <sub>SU</sub>	40			ns	R/W, RS
R/W and RS hold time	t <sub>h</sub>	10			ns	R/W, RS
Data output delay time	t <sub>D</sub>			120	ns	$DB_0 \sim DB_7$
Data hold time	t <sub>DH</sub>	20			ns	$DB_0 \sim DB_7$



#### • Interface mode with ,KS0063B

Characteristic	Symbol	Min.	Тур.	Max.	Unit	Test pin
Clock pulse width High	t <sub>CWH</sub>	800			ns	CLK
Clock pulse width Low	t <sub>CWL</sub>	800			ns	CLK
Data set-up time	t <sub>SU</sub>	300			ns	$DB_0 \sim DB_7$
Data hold time	t <sub>DH</sub>	300			ns	$DB_0 \sim DB_7$
Clock set-up time	t <sub>CSU</sub>	500			ns	CLK
M Delay time	t <sub>DM</sub>	-1000		1000	ns	М



# ■ OPERATING PRINCIPLES & METHODS

### ♦ Control and Display Command

Command	RS	R/W	DB7	DB <sub>6</sub>	DB5	$DB_4$	DB <sub>3</sub>	DB <sub>2</sub>	$\mathbf{DB}_1$	$\mathbf{DB}_0$	Executio n Time (f <sub>osc</sub> =	Remark
											250kHz)	
DISPLAY CLEAR RETURN HOME	L	L	L	L L	L L	L	L	L	L H	H X	1.64ms	Cursor move to first digit
ENTRY MODE	L L	L L	L L	L	L	L L	L L	L H	п I/D	SH	1.64ms 42µs	Cursor move to first digit • I/D : Set cursor move direction
SET	2	2	2	-	-	2	2		12	511	<i>τ2μ</i> 3	H Increase
												L Decrease
												• SH : Specifies shift of display
												H Display is shifted
												L Display is not shifted
DISPLAY ON/OFF	L	L	L	L	L	L	Н	D	С	В	42µs	• Display
												H Display on
												D L Display off
												• Cursor
												H Cursor on
												C L Cursor off
												Blinking
												H Blinking on
												B L Blinking off
SHIFT	L	L	L	L	L	Н	S/C	R/L	Х	Х	42µs	H Display shift
												S/C L Cursor move
												R/L H Right shift
												L Left shift
SET FUNCTION	L	L	L	L	Н	DL	N	F	Х	Х	42µs	H 8 bits interface
												L 4 bits interface
												H 2 line display
												N     L     1 line display
												H 5 X 10 dots
												F L 5 X 7 dots
SET CG RAM	L	L	L	Н			CG RAN	1 address	3		42µs	CG RAM Data is sent and
ADDRESS SET DD RAM	L	L	Н			(corres	ponds to RAM ad	cursor a				received after this setting DD RAM Data is sent and
ADDRESS											42µs	received after this setting
READ BUSY FLAG & ADDRESS	L	Н	BF		bo	Address oth DD &	Counter & CG RA				0µs	BF H Busy
												L Ready
												<ul> <li>Reads BF indication internal operating is being performed</li> </ul>
WRITE DATA	Н	L				M. mit.	e Data				46	<ul> <li>Reads address counter contents</li> <li>Write data into DD or CG RAM</li> </ul>
WALLE DATA	H	L H					Data				46µs	Read data from DD or CG RAM

#### ♦ Initializing by Internal Reset Circuit

The KS0076B automatically initializes (resets) when the power is on using the internal reset circuit. The following instruction are executed in initialization. The busy flag is kept in busy state (BF=1) until initialization ends. The busy state is 10ms after VDD rises to 4.5V.

- (1) Display Clear
- (2) Function Set
  - DL = 1: 8-bit interface data
  - N = 0: 1-line display
  - F = 0: 5x7-dot character font
- (3) Display On/Off Control
  - D = 0: Display Off
  - C = 0: Cursor Off
  - B = 0: Blink Off
- (4) Entry Mode Set I/D = 1 : +1 (Increment) S = 0 : No Shift

#### ♦ Initializing by Instruction

				Pow	er On					]
										_
	Wait f	or mo	re that	n 15m	s after	r VDD	) rises	to 4.5	V	
										_
RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0	BF cannot be checked before this instruction.
0	0	0	0	1	1	*	*	*	*	Function Set
										-
		,	Wait f	or mo	re tha	n 4.1n	ns			
										-
RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0	BF cannot be checked before this instruction.
0	0	0	0	1	1	*	*	*	*	Function Set
										-
		,	Wait f	or mo	re tha	n 100	us			
										-
RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0	BF cannot be checked before this instruction.
0	0	0	0	1	1	*	*	*	*	Function Set
										BF can be checked after following
										instruction.
										When BF is not checked, the waiting time
										between instructions is longer than execution
										instruction time.
										7
RS			DB6							Function Set (Specify the number of display
0	0	0	0	1	1	Ν	F	*	*	lines and character font.) The number of
										display lines and character font cannot be
										changed afterwards.
0	0	0	0	0	0	1	0	0	0	Display Off
0	0	0	0	0	0	0	0	0	1	Display Clear
0	0	0	0	0	0	0	1	I/I	)	Entry Mode Set
S										J
			Ŧ	• .• 1•	₩.					
			In	itializa	ation e	ends				

♦ Standard Character Pattern

upper 4 bit	0000	0010	0011	0100	0101	0110	0111	1000	1001	1010	1011	1100	1101	1110	1111
lower 4bit 0000	CG RAM					8									2 2 2 9 8 8 9 8 9 8 2 9
0001	(1)												888 <sub>2</sub> 2		
0010	(3)														
0011	(4)														
0100	(5)		2 2 2 2 2 2 2 3 2 2 3												
0101	(6)									3 W 3 M					
0110	(7)														
0111	(8)														
1000	(1)														
1001	(2)													9 2 2 2	
1010	(3)											192 92 94 94 94 940 940 940 940 940			
1011	(4)														
1100	(5)	8 W) W) 2													
1101	(6)									5 8 8 8 9 9 9 8 8					
1110	(7)	3 20 5 10								89 29 29 29 29 29 29 29 29 29 29 29 29 29 29 29 29 29 29 29 29 29 29 29 29 29 29 29 29 29 29 29 29 29 29 29 29 29 29 29 29 29 29 29 29 29 29 29 29 20 20 20 20 20 20 20 20 20 20 20 20 20			33		
1111	(8)												2 2 2 2 3 3 3 2		

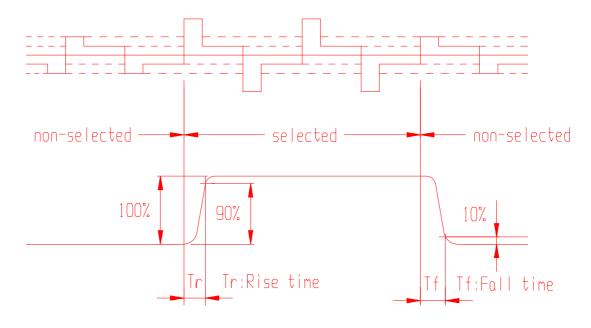
# ■ DISPLAY DATA RAM ADDRESS MAP

Characters	1	2	3	4	5	6	6	7	8	9	10	11	12	2 1	3 1	4	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
First line (H)	00	01	02	03	04	4 0	5 (	06	07	08	09	0A	70I	30	CC	)D	0E	0F	10	11	12	13	14	15	16	17	18	19	1A	1B	1C	1D	1E	1F	20	21	22	23	24	25	26	27
Second line	40	41	42	43	44	4	5	46	47	48	49	4 <i>F</i>	4H	34	C4	łD	4E	4F	50	51	52	53	54	55	56	57	58	59	5A	5B	5C	5D	5E	5F	60	61	62	63	64	65	66	67
Third line (H)	00	01	02	03	04	4 0	)5 (	06	07	08	09	0A	70I	30	CC	)D	0E	0F	10	11	12	13	14	15	16	17	18	19	1A	1B	1C	1D	1E	1F	20	21	22	23	24	25	26	27
Fourth line (H)	40	41	42	43	44	4 4	.5 4	46	47	48	49	4 <i>F</i>	\4I	34	C4	Đ	4E	4F	50	51	52	53	54	55	56	57	58	59	5A	5B	5C	5D	5E	5F	60	61	62	63	64	65	66	67

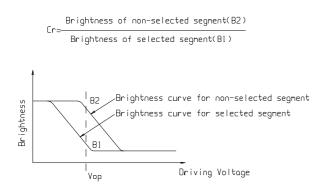
# ■ ELECTRO-OPTICAL CHARACTERISTICS (Vop = 5.0V, Ta = 25°C, Transflective version)

	Typ re	sponse	Typ re	sponse		]	Typ viewing	angle <b>q</b> (deg	g)
LCD mode	time T	r (ms)	time T	(ms)	Typ contrast				-
	Normal	Wide	Normal	Wide	ratio Cr	$\mathbf{A} = 0^{\circ}$	Æ = 90°	Æ = 180°	Æ = 270°
TN (A)	temp	temp	temp	temp	28	20	40	5	40
STN Y/G (B)					30	60	48	57	47
STN Blue (C)	075	1.47	<i>c</i> 1		6	52	25	33	33
STN Grey (D)	275	147	61	57	12	60	37	55	38
FSTN (F)					38	65	49	58	48
FSTN Negative (G)					18	53	25	34	33

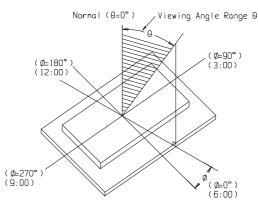
Note1: Definition of response time.



#### Note2: Definition of contrast ratio 'Cr'



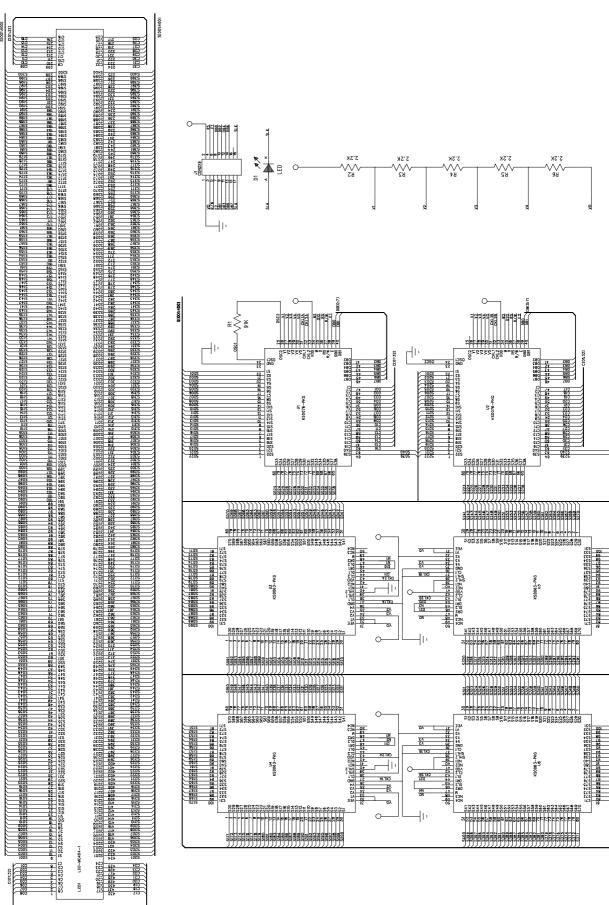
Note3: Definition of viewing angle range ' $\theta$ '.



# ■ INTERFACE PIN CONNECTIONS

Pin NO.	Symbol	Level	Description
1	VSS	0V	Ground
2	VDD	5.0V	Supply voltage for logic
3	VO		Input voltage for LCD
4	RS	H/L	H : Data signal, L : Instruction signal
5	R/W	H/L	H : Read mode, L : Write mode
6	E1	$H, H \to L$	Enable signal for first KS0076
7	E2	$H,H\to L$	Enable signal for second KS0076
8	DB0	H/L	Data bit 0
9	DB1	H/L	Data bit 1
10	DB2	H/L	Data bit 2
11	DB3	H/L	Data bit 3
12	DB4	H/L	Data bit 4
13	DB5	H/L	Data bit 5
14	DB6	H/L	Data bit 6
15	DB7	H/L	Data bit 7
16	NC		No connection
17	А		Backlight anode
18	K		Backlight cathode

# **CIRCUIT DIAGRAM**



# ■ RELIABILITY

# ◆ Content of Reliability Test

		<b>Environmental Test</b>		
No.	Test Item	Content of Test	Test Condition	Applicable Standard
1	High temperature	Endurance test applying the high storage	60 °C	
	storage	temperature for a long time.	200 hrs	
2	Low temperature	Endurance test applying the low storage	-10 °C	
	storage	temperature for a long time.	200 hrs	
3	High temperature	Endurance test applying the electric stress	50 °C	
	operation	(Voltage & Current) and the thermal stress to	200 hrs	
		the element for a long time.		
4	Low temperature	Endurance test applying the electric stress	0 °C	
	operation	under low temperature for a long time.	200 hrs	
5	High temperature /	Endurance test applying the high temperature	60 °C , 90 %RH	MIL-202E-103B
	Humidity storage	and high humidity storage for a long time.	96 hrs	JIS-C5023
6	High temperature /	Endurance test applying the electric stress	40 °C, 90 %RH	MIL-202E-103B
	Humidity operation	(Voltage & Current) and temperature /	96 hrs	JIS-C5023
		humidity stress to the element for a long time.		
7	Temperature cycle	Endurance test applying the low and high	-10°C / 60°C	
		temperature cycle.	10 cycles	
		-10°C 25°C 60°C		
		$_{30\min} \stackrel{\longrightarrow}{\frown} _{5\min} \stackrel{\longrightarrow}{\frown} _{30\min}$		
		1 cycle		
		Mechanical Test		1
8	Vibration test	Endurance test applying the vibration during	$10 \sim 22$ Hz $\rightarrow 1.5$ mmp-p	MIL-202E-201A
		transportation and using.	$22 \sim 500 \text{Hz} \rightarrow 1.5 \text{G}$	JIS-C5025
			Total 0.5hrs	JIS-C7022-A-10
9	Shock test	Constructional and mechanical endurance test	50G half sign	MIL-202E-213B
		applying the shock during transportation.	wave 11 msedc	
			3 times of each	
			direction	
10	Atmospheric	Endurance test applying the atmospheric	115 mbar	MIL-202E-105C
	pressure test	pressure during transportation by air.	40 hrs	
		Others		
11	Static electricity test	Endurance test applying the electric stress to	VS=800V , RS=1.5 k $\Omega$	MIL-883B-
		the terminal.	CS=100 pF	3015.1
			1 time	

\*\*\* Supply voltage for logic system = 5V. Supply voltage for LCD system = Operating voltage at 25°C.

# ♦ Failure Judgement Criterion

Criterion Item		Test Item No.									Failure Judgment Criterion
	1	1 2 3 4 5 6 7 8 9 10 11		11							
Basic specification										Out of the Basic Specification	
Electrical characteristic											Out of the DC and AC Characterstic
Mechanical characterstic											Out of the Mechanical Specification Color
											change : Out of Limit Apperance Specification
Optical characterstic							Out of the Apperance Standard				

# **QUALITY GUARANTEE**

#### ◆ Acceptable Quality Level

Each lot should satisfy the quality level defined as follows.

- Inspection method : MIL-STD-105E LEVEL II Normal one time sampling

- AOL

Partition AQL		Definition
A: Major	0.4%	Functional defective as product
B: Minor	1.5%	Satisfy all functions as product but not satisfy cosmetic standard

#### ♦ Definition of 'LOT'

One lot means the delivery quantity to customer at one time.

#### ◆ Conditions of Cosmetic Inspection

• Environmental condition

The inspection should be performed at the 1m of height from the LCD module under 2 pieces of 40W white fluorescent lamps (Normal temperature  $20 \sim 25^{\circ}$ C and normal humidity  $60 \pm 15\%$ RH).

- Inspection method
  - The visual check should be performed vertically at more than 30cm distance from the LCD panel.
- Driving voltage

The Vo value which the most optimal contrast can be obtained near the specified Vo in the specification. (Within  $\pm 0.5V$  of the typical value at 25°C.).

# ■ INSPECTION CRITERIA

#### ◆ Module Cosmetic Criteria

No.	Item	Judgement Criterion	Partition	
1	Difference in Spec.	None allowed	Major	
2	Pattern peeling	No substrate pattern peeling and floating	Major	
3	Soldering defects	No soldering missing	Major	
		No soldering bridge	Major	
		No cold soldering	Minor	
4	Resist flaw on substrate	Invisible copper foil (Ø0.5mm or more) on substrate pattern	Minor	
5	Accretion of metallic	No soldering dust	Minor	
	Foreign matter	No accretion of metallic foreign matters (Not exceed Ø0.2mm)	Minor	
6	Stain	No stain to spoil cosmetic badly	Minor	
7	Plate discoloring	No plate fading, rusting and discoloring	Minor	
8	Solder amount	a. Soldering side of PCB	Minor	
		Solder to form a 'Filet'		
	1. Lead parts	all around the lead.		
	-	Solder should not hide the		
		lead form perfectly. (too much)		
		b. Components side		
		(In case of 'Through Hole PCB')		
		Solder to reach the Components side of PCB.		
	2. Flat packages	Either 'toe' (A) or 'heal' (B) of	Minor	
		the lead to be covered by 'Filet'. A $B$		
		Lead form to be assume over		
		solder.		
	3. Chips	$(3/2) H \ge h \ge (1/2) H$	Minor	
	5. Chips	$(5/2)$ H $\geq$ H $\geq$ (1/2) H	WIIIOI	

No.	Defect	Jud	Partition						
1	Spots	In accordance with Screen Cosr	Minor						
2	Lines	In accordance with Screen Cosr	In accordance with Screen Cosmetic Criteria (Operating) No.2.						
3	Bubbles in polarizer	Size : d mm $d \le 0.3$ $0.3 < d \le 1.0$ $1.0 < d \le 1.5$ $1.5 < d$	$\begin{array}{c cccc} d \leq 0.3 & \text{Disregard} \\ 0.3 < d \leq 1.0 & 3 \\ 1.0 < d \leq 1.5 & 1 \end{array}$						
4	Scratch	In accordance with spots and lines operating cosmetic criteria. When the light reflects on the panel surface, the scratches are not to be remarkable.							
5	Allowable density	Above defects should be separa	Minor						
6	Coloration	Not to be noticeable coloration in the viewing area of the LCD panels.							
		Back-lit type should be judged with back-lit on state only.							
7	Contamination	Not to be noticeable.		Minor					

#### ◆ Screen Cosmetic Criteria (Non-Operating)

### ◆ Screen Cosmetic Criteria (Operating)

No.	Defect	Judgement Criterion	Partition Minor					
1	Spots	A) Clear						
		Size : d mm Acceptable Qty in active area						
		$d \le 0.1$ Disregard						
		$0.1 < d \le 0.2$ 6						
		$0.2 < d \le 0.3$ 2						
		0.3 < d 0						
		Note : Including pin holes and defective dots which must be within one pixel size. B) Unclear						
		Size : d mmAcceptable Qty in active area $d \le 0.2$ Disregard						
		$d \le 0.2 \qquad \text{Disregard} \\ 0.2 < d \le 0.5 \qquad 6$						
		$0.2 < d \le 0.5$ 0 $0.5 < d \le 0.7$ 2						
		$0.5 < d \le 0.7$ 0.7 < d $0$						
		0.7 \ \ \ U						
2	Lines	A) Clear	Minor					
		L 5.0 2.0 (6) (0) (0) (0) (0) (0) (0) (0) (1) (0) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1) (1						

'Clear' = The shade and size are not changed by Vo.

'Unclear' = The shade and size are changed by Vo.

No.	Defect	Judgement Criterion	Partition
3	Rubbing line	Not to be noticeable.	
4	Allowable density	Above defects should be separated more than 10mm each other.	Minor
5	Rainbow	Not to be noticeable.	Minor
6	Dot size	To be $95\% \sim 105\%$ of the dot size (Typ.) in drawing.	Minor
		Partial defects of each dot (ex. pin-hole) should be treated as 'spot'.	
		(see Screen Cosmetic Criteria (Operating) No.1)	
7	Uneven brightness	Uneven brightness must be BMAX / BMIN $\leq 2$	Minor
	(only back-lit type	- BMAX : Max. value by measure in 5 points	
	module)	- BMIN : Min. value by measure in 5 points	
		Divide active area into 4 vertically and horizontally.	
		Measure 5 points shown in the following figure.	
		0 0	
		0	
		0 0	
		O : Measuring points	

#### ◆ Screen Cosmetic Criteria (Operating) (Continued)

Note :

(1) Size : d = (long length + short length) / 2

(2) The limit samples for each item have priority.

(3) Complexed defects are defined item by item, but if the number of defects are defined in above table, the total number should not exceed 10.

(4) In case of 'concentration', even the spots or the lines of 'disregarded' size should not allowed. Following three situations should be treated as 'concentration'.

- 7 or over defects in circle of  $\emptyset$ 5mm.

- 10 or over defects in circle of  $\emptyset$ 10mm.
- 20 or over defects in circle of Ø20mm.

# ■ PRECAUTIONS FOR USING LCD MODULES

#### ♦ Handing Precautions

(1) The display panel is made of glass. Do not subject it to a mechanical shock by dropping it or impact.

(2) If the display panel is damaged and the liquid crystal substance leaks out, be sure not to get any in your mouth. If the substance contacts your skin or clothes, wash it off using soap and water.

(3) Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.

(4) The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully.

(5) If the display surface becomes contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If it is heavily contaminated, moisten cloth with one of the following solvents :

- Isopropyl alcohol
- Ethyl alcohol
- (6) Solvents other than those above-mentioned may damage the polarizer. Especially, do not use the following.
  - Water
  - Ketone
  - Aromatic solvents

(7) Exercise care to minimize corrosion of the electrode. Corrosion of the electrodes is accelerated by water droplets, moisture condensation or a current flow in a high-humidity environment.

(8) Install the LCD Module by using the mounting holes. When mounting the LCD module make sure it is free of twisting, warping and distortion. In particular, do not forcibly pull or bend the I/O cable or the backlight cable.

- (9) Do not attempt to disassemble or process the LCD module.
- (10) NC terminal should be open. Do not connect anything.

(11) If the logic circuit power is off, do not apply the input signals.

(12) To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.

- Be sure to ground the body when handling the LCD modules.

- Tools required for assembling, such as soldering irons, must be properly grounded.

- To reduce the amount of static electricity generated, do not conduct assembling and other work under dry conditions.

- The LCD module is coated with a film to protect the display surface. Exercise care when peeling off this protective film since static electricity may be generated.

#### ♦ Storage Precautions

When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps. Keep the modules in bags (avoid high temperature / high humidity and low temperatures below 0°C). Whenever possible, the LCD modules should be stored in the same conditions in which they were shipped from our company.

#### Others

Liquid crystals solidify under low temperature (below the storage temperature range) leading to defective orientation or the generation of air bubbles (black or white). Air bubbles may also be generated if the module is subject to a low temperature.

If the LCD modules have been operating for a long time showing the same display patterns, the display patterns may remain on the screen as ghost images and a slight contrast irregularity may also appear. A normal operating status can be regained by suspending use for some time. It should be noted that this phenomenon does not adversely affect performance reliability.

To minimize the performance degradation of the LCD modules resulting from destruction caused by static electricity etc., exercise care to avoid holding the following sections when handling the modules.

- Exposed area of the printed circuit board.

- Terminal electrode sections.

### ■ USING LCD MODULES

#### ◆ Liquid Crystal Display Modules

LCD is composed of glass and polarizer. Pay attention to the following items when handling.

(1) Please keep the temperature within specified range for use and storage. Polarization degradation, bubble generation or polarizer peel-off may occur with high temperature and high humidity.

(2) Do not touch, push or rub the exposed polarizers with anything harder than an HB pencil lead (glass, tweezers, etc.).

(3) N-hexane is recommended for cleaning the adhesives used to attach front/rear polarizers and reflectors made of organic substances which will be damaged by chemicals such as acetone, toluene, ethanol and isopropylalcohol.

(4) When the display surface becomes dusty, wipe gently with absorbent cotton or other soft material like chamois soaked in petroleum benzin. Do not scrub hard to avoid damaging the display surface.

(5) Wipe off saliva or water drops immediately, contact with water over a long period of time may cause deformation or color fading.

(6) Avoid contacting oil and fats.

(7) Condensation on the surface and contact with terminals due to cold will damage, stain or dirty the polarizers. After products are tested at low temperature they must be warmed up in a container before coming is contacting with room temperature air.

(8) Do not put or attach anything on the display area to avoid leaving marks on.

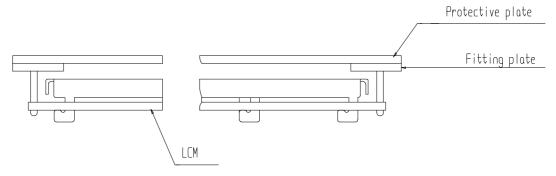
(9) Do not touch the display with bare hands. This will stain the display area and degradate insulation between terminals (some cosmetics are determinated to the polarizers).

(10) As glass is fragile. It tends to become or chipped during handling especially on the edges. Please avoid dropping or jarring.

#### ◆ Installing LCD Modules

The hole in the printed circuit board is used to fix LCM as shown in the picture below. Attend to the following items when installing the LCM.

(1) Cover the surface with a transparent protective plate to protect the polarizer and LC cell.



(2) When assembling the LCM into other equipment, the spacer to the bit between the LCM and the fitting plate should have enough height to avoid causing stress to the module surface, refer to the individual specifications for measurements. The measurement tolerance should be  $\pm 0.1$  mm.

#### Precaution for Handing LCD Modules

Since LCM has been assembled and adjusted with a high degree of precision, avoid applying excessive shocks to the module or making any alterations or modifications to it.

(1) Do not alter, modify or change the shape of the tab on the metal frame.

(2) Do not make extra holes on the printed circuit board, modify its shape or change the positions of components to be attached.

(3) Do not damage or modify the pattern writing on the printed circuit board.

(4) Absolutely do not modify the zebra rubber strip (conductive rubber) or heat seal connector.

(5) Except for soldering the interface, do not make any alterations or modifications with a soldering iron.

(6) Do not drop, bend or twist LCM.

#### ◆ Electro-Static Discharge Control

Since this module uses a CMOS LSI, the same careful attention should be paid to electrostatic discharge as for an ordinary CMOS IC.

(1) Make certain that you are grounded when handing LCM.

(2) Before remove LCM from its packing case or incorporating it into a set, be sure the module and your body have the same electric potential.

(3) When soldering the terminal of LCM, make certain the AC power source for the soldering iron does not leak.

(4) When using an electric screwdriver to attach LCM, the screwdriver should be of ground potentiality to minimize as much as possible any transmission of electromagnetic waves produced sparks coming from the commutator of the motor.

(5) As far as possible make the electric potential of your work clothes and that of the work bench the ground potential.

(6) To reduce the generation of static electricity be careful that the air in the work is not too dried. A relative humidity of 50%-60% is recommended.

#### Precaution for soldering to the LCM

(1) Observe the following when soldering lead wire, connector cable and etc. to the LCM.

- Soldering iron temperature :  $280^{\circ}C \pm 10^{\circ}C$ .
- Soldering time : 3-4 sec.
- Solder : eutectic solder.

If soldering flux is used, be sure to remove any remaining flux after finishing to soldering operation. (This does not apply in the case of a non-halogen type of flux.) It is recommended that you protect the LCD surface with a cover during soldering to prevent any damage dur to flux spatters.

(2) When soldering the electroluminescent panel and PC board, the panel and board should not be detached more than three times. This maximum number is determined by the temperature and time conditions mentioned above, though there may be some variance depending on the temperature of the soldering iron.

(3) When remove the electoluminescent panel from the PC board, be sure the solder has completely melted, the soldered pad on the PC board could be damaged.

#### Precautions for Operation

(1) Viewing angle varies with the change of liquid crystal driving voltage (Vo). Adjust Vo to show the best contrast.

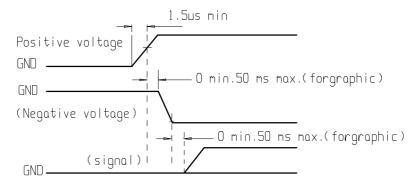
(2) Driving the LCD in the voltage above the limit shortens its life.

(3) Response time is greatly delayed at temperature below the operating temperature range. However, this does not mean the LCD will be out of the order. It will recover when it returns to the specified temperature range.

(4) If the display area is pushed hard during operation, the display will become abnormal. However, it will return to normal if it is turned off and then back on.

(5) Condensation on terminals can cause an electrochemical reaction disrupting the terminal circuit. Therefore, it must be used under the relative condition of 40°C, 50% RH.

(6) When turning the power on, input each signal after the positive/negative voltage becomes stable.



#### ♦ Storage

When storing LCDs as spares for some years, the following precaution are necessary.

(1) Store them in a sealed polyethylene bag. If properly sealed, there is no need for dessicant.

(2) Store them in a dark place. Do not expose to sunlight or fluorescent light, keep the temperature between  $0^{\circ}$ C and  $35^{\circ}$ C.

(3) The polarizer surface should not come in contact with any other objects. (We advise you to store them in the container in which they were shipped.)

(4) Environmental conditions :

- Do not leave them for more than 168hrs. at 60°C.

- Should not be left for more than 48hrs. at -20°C.

#### ♦ Safety

(1) It is recommended to crush damaged or unnecessary LCDs into pieces and wash them off with solvents such as acetone and ethanol, which should later be burned.

(2) If any liquid leakes out of a damaged glass cell and comes in contact with the hands, wash off thoroughly with soap and water.

#### ♦ Limited Warranty

Unless agreed between DISPLAYTECH and customer, DISPLAYTECH will replace or repair any of its LCD modules which are found to be functionally defective when inspected in accordance with DISPLAYTECH LCD acceptance standards (copies available upon request) for a period of one year from date of shipments. Cosmetic/visual defects must be returned to DISPLAYTECH within 90 days of shipment. Confirmation of such date shall be based on freight documents. The warranty liability of DISPLAYTECH limited to repair and/or replacement on the terms set forth above. DISPLAYTECH will not be responsible for any subsequent or consequential events.

#### ◆ Return LCM under warranty

No warranty can be granted if the precautions stated above have been disregarded. The typical examples of violations are :

- Broken LCD glass.
- PCB eyelet's damaged or modified.
- PCB conductors damaged.
- Circuit modified in any way, including addition of components.
- PCB tampered with by grinding, engraving or painting varnish.
- soldering to or modifying the bezel in any manner.

Module repairs will be invoiced to the customer upon mutual agreement. Modules must be returned with sufficient description of the failures or defects. Any connectors or cable installed by the customer must be removed completely without damaging the PCB eyelet's, conductors and terminals.